

Amendments to the Claims:

1. (Currently Amended) A method for configuring a plurality of controlling devices during a system reset of a computing device which includes at least a first controlling device, a second controlling device, and a data device that contains configuration data, the method comprising:

in response to a system reset of a computing device, resetting the data device and the first controlling device;

transmitting first configuration data from a ~~the~~ data device to a ~~first one of the~~ first controlling device[[s]] to configure the first controlling device; and

causing the first controlling device to reset the second controlling device; and

transmitting second configuration data from the data device to a ~~second one of the~~ second controlling device[[s]].

2. (Original) The method for configuring a plurality of controlling devices as set forth in claim 1,

further including:

before transmitting the first configuration data, transmitting a first timing signal from the first controlling device to the data device; and

wherein transmitting the first configuration data includes:

transmitting the first configuration data from the data device to the first controlling device as a function of the first timing signal.

3. (Original) The method for configuring a plurality of controlling devices as set forth in claim 2, further including:

before transmitting the second configuration data, transmitting a reset signal from the first controlling device to the second controlling device.

4. (Original) The method for configuring a plurality of controlling devices as set forth in claim 3,

further including:

before transmitting the second configuration data, transmitting a second timing signal from the second controlling device to the data device; and

wherein transmitting the second configuration data includes:

transmitting the second configuration data from the data device to the second controlling device as a function of the second timing signal.

5. (Original) The method for configuring a plurality of controlling devices as set forth in claim 4, wherein transmitting the second configuration data further includes:

transmitting the second configuration data from the data device to the second controlling device as a function of the first timing signal.

6. (Currently Amended) The method for configuring a plurality of controlling devices as set forth in claim 4, wherein transmitting the second timing signal includes:

transmitting the second timing signal from the second controlling device to the data device without passing through the first controlling device.

7. (Original) the method for configuring a plurality of controlling devices as set forth in claim 1, wherein transmitting the second configuration data includes:

transmitting the second configuration data from the data device to the second controlling device via the first controlling device.

8. (Original) the method for configuring a plurality of controlling devices as set forth in claim 1, wherein transmitting the second configuration data includes:

transmitting the second configuration data from the data device to the second controlling device without passing through the first controlling device.

9. (Original) The method for configuring a plurality of controlling devices as set forth in claim 1, wherein at least one of the transmitting the first and second configuration data includes:

serially transmitting the configuration data from the data device to the respective one of the controlling devices.

10-15. Canceled.

16. (Currently Amended) The ~~signals executable on the~~ computing device as set forth in claim ~~15~~ 21, ~~further including where the computing device being further configured to transmit:~~

signals for causing the configuration data ~~signals~~ to be transmitted from the data device to the ~~first and second controlling devices~~ processor and the configurable logic device serially.

17. (Currently Amended) The ~~signals executable on the~~ computing device as set forth in claim ~~15~~ 21, ~~further including where the computing device being further configured to transmit:~~

signals for causing a first ~~of the~~ control signal[[s]] to be transmitted from the ~~first controlling configurable logic~~ device to the data device, a first portion of the configuration data signals being transmitted from the data device to the ~~first controlling configurable logic~~ device as a function of the first control signal; and

signals for causing a second ~~of the~~ control signal[[s]] to be transmitted from the ~~second controlling device processor~~ to the data device, a second portion of the configuration data being transmitted from the data device to the ~~second controlling device processor~~ as a function of the second control signal.

18. (Currently Amended) The ~~signals executable on the~~ computing device as set forth in claim 17, ~~further including where the computing device being further configured to transmit:~~

signals for causing the second control signal and the second portion of the configuration data to be transmitted from the data device to the ~~second controlling device processor~~ via the ~~first controlling configurable logic~~ device.

19. (Currently Amended) The ~~signals executable on the~~ computing device as set forth in claim 18, ~~further including where the computing device being further configured to transmit:~~

signals for causing the second portion of the configuration data to be transmitted from the data device to the second controlling device processor as a function of both the first and second control signals.

20. (Currently Amended) ~~The the signals executable on the computing device as set forth in claim 17, further including where the computing device being further configured to transmit:~~

signals for causing the second control signal and the second portion of the configuration data to be communicated between the data device to the second controlling device processor without passing through the first controlling configurable logic device.

21. (New) A computing device comprising:

a processor;

a configurable logic device operably connected to the processor and being configured to provide a processor reset signal to the processor;

a data device for maintaining configuration data to the processor and the configurable logic device, the data device being in communication with the processor and the configurable logic device;

a power supply connected to the configurable logic device and the data device for providing a system reset signal in response to a reset of the computing device,

the configurable logic device being configured to initiate receiving of configuration data from the data device in response to the system reset signal;

the configurable logic device being further configured to transmit a processor reset signal to the processor upon completion of configuration of the configurable logic device; and

the processor being configured to initiate receiving of configuration data from the data device in response to the processor reset signal.